

<b>Notice of References Cited</b>	Application/Control No. 09/964,765		Applicant(s)/Patent Under Reexamination DOYLE ET AL.	
	Examiner Enrique L Santiago		Art Unit 2671	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,808,618	09-1998	Kawano et al.	345/422
*	B	US-5,629,769	05-1997	Cookingham et al.	356/243.1
*	C	US-4,563,548	01-1986	Misherghi et al.	379/357.04
*	D	US-4,257,096	03-1981	McCullough et al.	718/106
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	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Article: "Optimal Depth Buffer for low-Cost Graphics Hardware", by Eugene Lapidous*, Guofang Jiao*, Trident Microsystems Inc., pages 67-73, *2450 Walsh Avenue, Santa Clara CA 95051, ACM 1999 1-58113-170-4/99/08.
	V	Definition of W-Buffer, Smart Computing (online) <a href="http://www.smartcomputing.com/">http://www.smartcomputing.com/</a>
	W	Definition of Buffer, Dictionary of Computers (on line) <a href="http://www.tiscali.co.uk/reference/dictionaries/computers/data/content_a.html">http://www.tiscali.co.uk/reference/dictionaries/computers/data/content_a.html</a>
	X	Definition of Register, Dictionary of Computers (on line) <a href="http://www.tiscali.co.uk/reference/dictionaries/computers/data/content_a.html">http://www.tiscali.co.uk/reference/dictionaries/computers/data/content_a.html</a>

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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